

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application

Inventor(s): Krivokapic, et al.

SC/Serial No.: 09/711,445

Confirm. No.: 7218

Filed: November 13, 2000

Title: SELF-ALIGNED TRIPLE GATE SILICON-ON-INSULATOR DEVICE

PATENT APPLICATION

Art Unit: 2811

Examiner: Owens, Douglas W.

Customer No. 23910

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(NEJ)
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CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited in the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Art Unit 2811, Alexandria, VA 22313-1450, on Date.

S.R.B.

_____(Attorney Signature)

Stephen R. Bachmann, Reg. No. 50,806

Signature Date: October 6, 2003

RESPONSE D TO OFFICE ACTION UNDER 37 C.F.R. § 1.111

Commissioner for Patents
P.O. Box 1450
Art Unit 2811,
Washington, DC 20231

Sir:

This RESPONSE D is in reply to the Office action mailed June 4, 2003. An appropriate Petition for Extension of Time to Respond is submitted herewith, together with the appropriate fee.

Amendments

Please amend the above-identified application as follows:

10/15/2003 AKELLEY 00000003 09711445

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OFFICE OF PETITIONS

In the Drawings:

Enclosed are formal drawing substitute sheets for Figs. 1-16C, which include Applicant's desired changes, without markings, and comply with § 1.84. Subject to the approval of the Examiner, it is respectfully requested that the new drawing sheets be substituted for the originally filed drawing sheets for Figs. 1-16A. The changes are explained in the Remarks section of the present Response.

In the Title:

Please replace the title with the new title shown below:

SELF-ALIGNED TRIPLE GATE SILICON-ON-INSULATOR DEVICE